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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/775,231	02/01/2001	Han-Sin Lee	SAM-167	9048

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EXAMINER

POMPEY, RON EVERETT

ART UNIT

PAPER NUMBER

2812

DATE MAILED: 03/14/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

Offic Action Summary	Application No.	Applicant(s)
	09/775,231	LEE ET AL.
	Examiner	Art Unit
	Ron E Pompey	2812

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on _____.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-12 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 1-12 is/are rejected.
- 7) Claim(s) _____ is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) The proposed drawing correction filed on _____ is: a) approved b) disapproved by the Examiner.
If approved, corrected drawings are required in reply to this Office action.
- 12) The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.
- 14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
 - a) The translation of the foreign language provisional application has been received.
- 15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) Notice of References Cited (PTO-892)
- 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s) 2 .
- 4) Interview Summary (PTO-413) Paper No(s) _____.
- 5) Notice of Informal Patent Application (PTO-152)
- 6) Other:

DETAILED ACTION

Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claims 1-12 are rejected under 35 U.S.C. 103(a) as being unpatentable over Laparra et al. (US 6,319,796) and in further view of Park et al. (US 6,326,282).

Laparra discloses the steps of:

For claims 1-6:

etching the exposed semiconductor substrate, using the etching mask pattern as an etching mask, to form a trench;

providing a material layer (40b, fig. 4) on the insulating layer (40a, fig. 4) filling the trench;

planarily etching the material layer and the insulating layer down to a top surface of the etching mask pattern to form a device isolation layer pattern in the trench; and removing the exposed etching mask pattern (col. 4, Ins. 5-10 and col. 5, Ins. 51-56).

3. Laparra discloses all the steps of claims 1-12 except those described below.

However Park discloses the steps of:

For claims 8-12:

prior to forming the insulating layer:

forming an oxide layer (112, fig. 2D) on the inner wall and bottom of the trench; forming an oxidation barrier layer (114, fig. 2D) on the oxide layer; and further comprising forming a capping layer (115, fig. 2D) between the oxidation barrier layer and the insulating layer (col. 4, Ins. 29-52).

Therefore it would have been obvious to those of ordinary skill in the art to combine Park with Laparra, because the layers disclosed by Park are protection layers for the trench.

4. Laparra and Park disclose all the steps of claims 1-12 except for the material layer being formed being formed at a temperature of 500°C and higher and forming a etch-stop layer comprising polysilicon and a HTO layer which are sequentially stacked. However, the examiner takes official notice that an LPCVD TEOS film is formed in the temperature range of 500°C and higher. Also, that it would have been to one having ordinary skill in the art at the time the invention was made to form an etch-stop layer, comprising polysilicon and a HTO layer which are sequentially stacked, since it has been held to be within the general skill of a worker in the art to select a known material on the basis of its suitability for the intended use as a matter of obvious design choice.

In re Leshin, 125 USPQ 416.

Specification

5. The disclosure is objected to because of the following informalities: Applicant claims, in claim 9, a thickness range of 20Å - 300Å. However the specification only states a thickness range of 20Å - 200Å. Also, Applicant claims, in claim 10, an oxidation barrier layer comprising silicon nitride. However the specification does not state what the oxidation barrier layer is comprised of.

Appropriate correction is required.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ron E Pompey whose telephone number is (703) 305-3016. The fax number is assigned are (703) 305-3432.

Ron Pompey
Ron Pompey
Art Unit: 2812
March 10, 2002

J. Niebling
John F. Niebling
Supervisory Patent Examiner
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